

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/692,561
12 <sup>th</sup> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 18, 2000
	Inventors: Laurence B. Boucher et al.
	Group Art Unit: 2157
INTELLIGENT NETWORK INTERFACE SYSTEM AND METHOD FOR ACCELERATED PROTOCOL PROCESSING	Examiner name: Moustafa M. Meky
	Attorney Docket No. ALA-002A

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U.S. Patent Documents						
Examiner Initial		Document	Date	Name	Class	Subclass
	1	4,366,538	December 28, 1982	Johnson et al.	364	200
	2	4,485,455	November 27, 1984	Gary W. Boone et al	364	900
	3	4,485,460	November 27, 1984	Mark A. Stambaugh	365	203
	4	4,589,063	May 13, 1986	Shah et al.	710	8
	5	4,700,185	October 13, 1987	Thomas J. Balph et al	340	825.5
	6	4,991,133	May 13, 1986	Davis et al.	364	900
	7	5,056,058	October 8, 1991	Hirata et al.	709	230
	8	5,058,110	October 15, 1991	Beach et al.	370	85.6
	9	5,097,442	March 17, 1992	Ward et al.	365	78
	10	5,163,131	November 10, 1992	Row et al.	395	200
	11	5,212,778	May 18, 1993	Dally et al.	395	400
	12	5,280,477	January 18, 1994	Trapp	370	85.1
	13	5,289,580	February 22, 1994	Latif et al.	395	275
	14	5,303,344	April 12, 1994	Yokoyama et al.	395	200
	15	5,412,782	May 2, 1995	Hausman et al.	395	250
	16	5,418,912	May 23, 1995	David A. Christenson	395	200
	17	5,448,566	September 5, 1995	Richter et al.	370	94.1
	18	5,485,579	January 16, 1996	Hitz et al.	395	200.12
	19	5,506,966	April 9, 1996	Ban	395	250
	20	5,511,169	April 23, 1996	Suda	395	280
	21	5,517,668	May 14, 1996	Szwerinski et al.	395	800
	22	5,524,250	June 4, 1996	Chesson et al.	395	775
	23	5,535,375	July 9, 1996	Eshel et al.	391	500

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	24	5,548,730	August 20, 1996	Young et al.	395	280	
	25	5,566,170	October 15, 1996	Bakke et al.	370	60	
	26	5,574,919	November 12, 1996	Arun N. Netravali et al	395	561	
	27	5,566,170	October 15, 1996	Bakke et al.	370	60	
	28	5,588,121	December 24, 1996	Reddin et al.	395	200.15	
	29	5,590,328	December 31, 1996	Seno et al.	395	675	
	30	5,592,622	January 7, 1997	Isfeld et al.	395	200.02	
	31	5,598,410	January 28, 1997	Stone	370	469	
	32	5,619,650	April 8, 1997	Bach et al.	395	200.01	
	33	5,629,933	May 13, 1997	Delp et al.	370	411	
	34	5,633,780	May 27, 1997	Cronin et al.	361	220	
	35	5,634,099	May 27, 1997	Andrews et al.	395	200.07	
	36	5,634,127	May 27, 1997	Cloud et al.	395	680	
	37	5,642,482	June 24, 1997	Pardillos	395	200.2	
	38	5,664,114	September 2, 1997	Krech, Jr. et al.	395	200.64	
	39	5,671,355	September 23, 1997	Collins	395	200.2	
	40	5,678,060	October 14, 1997	Yokoyama et al.	709	212	
	41	5,682,534	October 28, 1997	Kapoor et al.	395	684	
	42	5,692,130	November 25, 1997	Shobu et al.	395	200.12	
	43	5,699,317	December 16, 1997	Sartore et al.	395	230.06	
	44	5,699,350	December 16, 1997	Andrew J. Kraslavsky	370	254	
	45	5,701,434	December 23, 1997	Nakagawa	395	484	
	46	5,701,516	December 23, 1997	Cheng et al.	395	842	
	47	5,727,142	March 10, 1998	Chen	395	181	
	48	5,742,765	April 21, 1998	Wong et al.	395	200	

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	49	5,749,095	May 5, 1998	Hagersten	711	141	
	50	5,751,715	May 12, 1998	Chan et al.	370	455	
	51	5,752,078	May 12, 1998	Delp et al.	395	827	
	52	5,758,084	May 26, 2008	Silverstein et al.	395	200.58	
	53	5,758,089	May 26, 2008	Gentry et al.	395	200.64	
	54	5,758,186	May 26, 2008	Hamilton et al.	395	831	
	55	5,758,194	May 26, 2008	Kuzma	395	886	
	56	5,768,618	June 16, 1998	Gene R. Erickson et al	395	829	
	57	5,771,349	June 23, 1998	Picazo, Jr. et al.	395	188.01	
	58	5,774,660	June 30, 1998	Brendel et al.	395	200.31	
	59	5,778,013	July 7, 1998	Jedwab	714	807	
	60	5,778,419	July 7, 1998	Hansen et al.	711	112	
	61	5,790,804	August 4, 1998	Osborne	709	245	
	62	5,794,061	August 11, 1998	Hansen et al.	395	800.01	
	63	5,802,258	September 1, 1998	Chen	395	182.08	
	64	5,802,580	September 1, 1998	McAlpice	711	149	
	65	5,809,328	September 15, 1998	Nogales et al.	395	825	
	66	5,809,527	September 15, 1998	Cooper et al.	711	133	
	67	5,812,775	September 22, 1998	Van Seeters et al.	395	200.43	
	68	5,815,646	September 29, 1998	Purcell et al.	395	163	
	69	5,828,835	October 27, 1998	Mark S. Isfeld et al	395	200.3	
	70	5,848,293	December 8, 1998	Gentry et al.	395	825	
	71	5,872,919	February 16, 1999	Wakeland et al.	395	200	
	72	5,878,225	March 2, 1999	Bilansky et al.	395	200.57	
	73	5,892,903	April 6, 1999	Christopher W. Klaus	395	187.01	

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	74	5,898,713	April 27, 1999	Melzer et al.	371	53	
	75	5,913,028	June 15, 1999	Wang, et al.	395	200.33	
	76	5,920,566	July 6, 1999	Ariel Hendel et al	370	401	
	77	5,930,830	July 27, 1999	Mendelson et al.	711	171	
	78	5,931,918	August 3, 1999	Row et al.	709	300	
	79	5,935,205	August 10, 1999	Murayama et al.	709	216	
	80	5,937,169	August 10, 1999	Connery et al.	395	200.8	
	81	5,941,969	August 24, 1999	Ram et al.	710	128	
	82	5,941,972	August 24, 1999	Hoese et al.	710	129	
	83	5,950,203	September 7, 1999	Stakuis et al.	707	10	
	84	5,970,804	August 4, 1998	Osborne	395	200.75	
	85	5,987,022	November 16, 1999	Robert L. Geiger et al	370	349	
	86	5,991,299	November 23, 1999	Radogna et al.	370	392	
	87	5,996,013	November 30, 1999	Gary Scott Delp et al	709	226	
	88	5,996,024	November 30, 1999	Blumenau	709	301	
	89	6,005,849	December 21, 1999	Roach et al.	370	276	
	90	6,009,478	December 28, 1999	Panner, et al.	710	5	
	91	6,016,513	January 18, 2000	Glen H. Lowe	709	250	
	92	6,021,446	February 1, 2000	Gentry et al.	709	303	
	93	6,021,507	February 1, 2000	Shawfu Chen	714	2	
	94	6,026,452	February 15, 2000	William Michael Pitts	710	56	
	95	6,034,963	March 7, 2000	Minami et al.	370	401	
	96	6,038,562	March 14, 2000	Anjur et al.	707	10	
	97	6,041,058	March 21, 2000	John A. Flanders et al	370	401	
	98	6,041,381	March 21, 2000	Geoffrey B. Hoese	710	129	

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	99	6,044,438	March 28, 2000	Howard Thomas Olnowich	711	130	
	100	6,047,323	April 4, 2000	Michael R. Krause	709	227	
	101	6,047,356	April 4, 2000	Anderson et al.	711	129	
	102	6,049,528	April 11, 2000	Ariel Hendel et al	370	235	
	103	6,057,863	May 2, 2000	Sompong P. Olarig	345	520	
	104	6,061,368	May 9, 2000	Hitzelberger	370	537	
	105	6,065,096	May 16, 2000	Day et al.	711	114	
	106	6,067,569	May 23, 2000	Mohamed J. Khaki et al	709	224	
	107	6,070,200	May 30, 2000	Gates et al.	710	20	
	108	6,078,733	June 20, 2000	Randy B. Osborne	395	200.8	
	109	6,097,734	August 1, 2000	Joel Gotesman et al	370	474	
	110	6,101,555	August 8, 2000	Goshey et al.	709	321	
	111	6,111,673	August 29, 2000	Gee-Kung Chang et al	359	123	
	112	6,115,615	September 5, 2000	Takeshi Ota et al	455	553	
	113	6,122,670	September 19, 2000	Toby D. Bennett et al	709	236	
	114	6,141,701	October 31, 2000	Mark M. Whitney	710	5	
	115	6,141,705	October 31, 2000	Anand et al.	710	15	
	116	6,145,017	November 7, 2000	Ghaffari	710	5	
	117	6,157,955	December 5, 2000	Narad et al.	709	228	
	118	6,172,980	January 9, 2001	Flanders et al.	370	401	
	119	6,173,333	July 18, 1997	Jolitz et al.			
	120	6,181,705	February 24, 2004	San-Hong Kim	370	392	
	121	6,202,105	March 13, 2001	Gates et al.	710	20	
	122	6,223,242	April 24, 2001	Stephen J. Sheafor et al	710	132	
	123	6,226,680	May 1, 2001	Boucher et al.	709	230	

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	124	6,246,683	June 12, 2001	Connery et al.	370	392	
	125	6,247,060	June 12, 2001	Boucher et al.	709	238	
	126	6,279,051	August 21, 2001	Gates et al.	710	20	
	127	6,289,023	September 11, 2001	Brian M. Dowling et al	370	419	
	128	6,298,403	October 2, 2001	Suri et al.	710	100	
	129	6,324,649	November 27, 2001	Kevin W. Eyres et al	713	202	
	130	6,334,153	December 25, 2001	Boucher et al.	709	230	
	131	6,343,360	January 29, 2002	David Feinleib	713	1	
	132	6,345,301	February 5, 2002	Burns et al.	709	230	
	133	6,345,302	February 5, 2002	Toby D. Bennett et	709	236	
	134	6,356,951	March 12, 2002	Gentry et al.	709	250	
	135	6,370,599	April 9, 2002	Sanjay Anand et al	710	15	
	136	6,385,647	May 7, 2002	Dean Willis et al	709	217	
	137	6,389,468	May 14, 2002	Muller et al.	709	226	
	138	6,389,479	May 14, 2002	Boucher	709	243	
	139	6,393,487	May 21, 2002	Boucher, et al.	709	238	
	140	6,421,742	July 16, 2002	Tillier	710	1	
	141	6,421,753	July 16, 2002	Geoffrey B. Hoese et al	710	129	
	142	6,427,169	July 30, 2002	Elzur	709	224	
	143	6,427,171	July 30, 2002	Craft, et al.	709	230	
	144	6,427,173	July 30, 2002	Laurence B. Boucher et al	709	238	
	145	6,434,620	August 13, 2002	Boucher, et al.	709	230	
	146	6,434,651	August 13, 2002	Gentry, Jr.	710	260	
	147	6,449,656	September 10, 2002	Elzur et al.	709	236	
	148	6,453,360	September 17, 2002	Muller et al.	709	250	

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	149	6,470,415	October 22, 2002	Starr, et al.	711	104	
	150	6,473,425	October 29, 2002	Gilles Bellaton et al	370	392	
	151	6,480,489	November 12, 2002	Muller et al.	370	389	
	152	6,487,202	November 26, 2002	Daniel E. Klausmeier et al	370	395	
	153	6,487,654	November 26, 2002	Eric M. Dowling	712	244	
	154	6,490,631	December 3, 2002	Paul R. Teich et al	709	250	
	155	6,502,144	December 31, 2002	Jean-Paul Accarie	710	8	
	156	6,523,119	February 18, 2003	Dominique Vincent Pavlin et al	713	192	
	157	6,526,446	February 25, 2003	Yang et al.	709		
	158	6,570,884	May 27, 2003	Glenn William Connery et al	370	419	
	159	6,591,310	July 8, 2003	Stephen B. Johnson	710	3	
	160	6,591,302	July 8, 2003	Boucher, et al.	709	230	
	161	6,648,611	November 18, 2003	David M. Morse et al	417	310	
	162	6,650,640	November 18, 2003	Shimon Muller et al	370	392	
	163	6,657,757	December 2, 2003	Gee-Kung Chang et al	359	124	
	164	6,658,480	December 2, 2003	Laurence B. Boucher et al	709	239	
	165	6,678,283	January 13, 2004	Yakov Teplitsky	370	463	
	166	6,681,364	January 20, 2004	Jean Louis Calvignac et al	714	776	
	167	6,687,758	February 3, 2004	Craft, et al.	709	250	
	168	6,697,868	February 24, 2004	Peter K. Craft et al	709	230	
	169	6,751,665	June 15, 2004	Philbrick, et al.	709	224	
	170	6,765,901	July 20, 2004	Michael Ward Johnson et al	370	352	
	171	6,757,746	June 29, 2004	Boucher, et al.	709	250	
	172	6,807,581	October 19, 2004	Daryl D. Starr et al	709	250	
	173	6,842,896	January 11, 2005	Mark E. Redding et al	717	172	

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	174	6,912,522	June 28, 2005	David A. Edgar	707	2	
	175	6,938,092	August 30, 2005	Burns	709	230	
	176	6,941,386	September 6, 2005	Peter K. Craft et al	709	250	
	177	6,965,941	November 15, 2005	Laurence B. Boucher et al	709	230	
	178	6,996,070	February 7, 2006	Starr, et al.	370	252	
	179	7,042,898	May 9, 2006	Blightman, et al.	370	463	
	180	7,076,568	July 11, 2006	Philbrick, et al.	709	250	
	181	7,089,326	August 8, 2006	Boucher, et al.	709	242	
	182	7,093,099	August 15, 2006	Bodas, et al.	711	206	
	183	7,124,205	October 17, 2006	Craft, et al.	709	250	
	184	7,133,940	November 7, 2006	Blightman, et al.	710	22	
	185	7,167,926	January 23, 2007	Boucher, et al.	709	250	
	186	7,167,927	January 23, 2007	Philbrick, et al.	709	250	
	187	7,174,393	February 6, 2007	Boucher, et al.	709	250	
	188	7,185,266	February 27, 2007	Blightman, et al.	714	776	
	189	7,191,241	March 13, 2007	Boucher, et al.	709	230	
	190	7,191,318	March 13, 2007	Tripathy, et al.	712	225	
	191	7,237,036	June 26, 2007	Boucher, et al.	709	245	
	192	7,254,696	August 7, 2007	Mittal, et al.	712	210	
	193	7,284,070	October 16, 2007	Boucher, et al.	709	250	

Published Applications

Examiner							
Initial		Document	Date	Name	Class	Subclass	
	194	2001/0004354	January 10, 2001	Jolitz	370	328	
	195	2001/0013059	August 9, 2001	Dawson et al.	709	217	

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	196	2001/0014892	August 16, 2001	Gaither et al.	707	200	
	197	2001/0014954	August 16, 2001	Purcell et al.	714	4	
	198	2001/0025315	January 10, 2001	Jolitz	709	231	
	199	2001/0048681	December 6, 2001	Bilic et al.	370	389	
	200	2001/0053148	December 20, 2001	Bilic et al.	370	389	
	201	2002/0073223	June 13, 2002	B. Scott Darnell et al	709	232	
	202	2002/0112175	August 15, 2002	Makofka et al	713	200	
	203	2003/0066011	April 11, 2002	Oren	714	758	
	204	2003/0110344	June 1, 2003	Szezepanek et al.	711	100	
	205	2003/0165160	September 4, 2003	Minami et al.	370	466	
	206	2004/0054814	March 1, 2004	McDaniel			
	207	2004/0059926	March 25, 2004	Angelo, et al.	713	168	
	208	2004/0153578	August 1, 2004	Elzur			
	209	2004/0213290	October 1, 2004	Johnson et al.	370	469	
	210	2004/0246974	December 9, 2004	Gyugyi et al.	370	395.31	

#### Foreign Patent Documents

Examiner Initial		Document Number	Date	Country	Class	Subclass	
	211	WO 98/19412	May 7, 1998	PCT/US97/17257			
	212	WO 98/50852	November 12, 1998	PCT/US98/08719			
	213	WO 99/04343	January 28, 1999	PCT/US98/14729			
	214	WO 99/65219	December 16, 1999	PCT/US/99/13184			
	215	WO 00/13091	March 9, 2000	PCT/US98/24943			
	216	WO 01/04770	January 18, 2001	PCT/US00/18939			
	217	WO 01/05107	January 18, 2001	PCT/US00/19006			

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	218	WO 01/05116	January 18, 2001	PCT/US00/19243			
	219	WO 01/05123	January 18, 2001	PCT/US00/18976			
	220	WO 01/40960	June 7, 2001	PCT/US00/32660			
	221	WO 01/59966	August 16, 2001	PCT/US00/06475			
	222	WO 01/86430	November 15, 2001	PCT/US01/15180			

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

	223	Internet pages entitled "Hardware Assisted Protocol Processing", (which Eugene Feinber is working on), 1 page, printed 11/25/98.
	224	Zilog product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages, 1997.
	225	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.
	226	Internet pages entitled: Asante and 100BASE-T Fast Ethernet. 7 pages, printed 5/27/97.
	227	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University. 13 pages, printed 12/21/98.
	228	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	229	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	230	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996, 15 pages.
	231	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
	232	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
	233	iReady News Archives article entitled "iReady Rounding Out Management Team with Two Key Executives", <a href="http://www.ireadyco.com/archives/keyexec.html">http://www.ireadyco.com/archives/keyexec.html</a> , 2 pages, printed 11/28/98.
	234	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based On iReady's Design," Press Release October, 1998, 3 pages, printed 11/28/98.
	235	Internet pages from iReady Products, web site <a href="http://www.ireadyco.com/products.html">http://www.ireadyco.com/products.html</a> , 2 pages, downloaded 11/25/98.
	236	iReady News Archives, Toshiba, iReady shipping Internet chip, 1 page, printed 11/25/98.

Examiner	Date Considered
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	Inventors: Laurence B. Boucher et al.
	Group Art Unit: 2157
<b>INTELLIGENT NETWORK INTERFACE SYSTEM AND METHOD FOR ACCELERATED PROTOCOL PROCESSING</b>	Examiner name: Moustafa M. Meky
	Attorney Docket No. ALA-002A

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237	Interprophet article entitled "Technology", <a href="http://www.interprophet.com/technology.html">http://www.interprophet.com/technology.html</a> , 17 pages, printed 3/1/00.
238	iReady Corporation, article entitled "The I-1000 Internet Tuner", 2 pages, date unknown.
239	iReady article entitled "About Us Introduction", Internet pages from <a href="http://www.iReadyco.com/about.html">http://www.iReadyco.com/about.html</a> , 3 pages, printed 11/25/98.
240	iReady News Archive article entitled "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA, November 20, 1997. 2 pages, printed 11/2/98.
241	iReady News Archive article entitled "Seiko Instruments Inc. (SII) INTRODUCES WORLD'S FIRST INTERNET-READY INTELLIGENT LCD MODULES BASED ON IREADY TECHNOLOGY," Santa Clara, CA and Chiba, Japan, October 26, 1998. 2 pages, printed 11/2/98.
242	NEWSwatch article entitled "iReady internet Tuner to Web Enable Devices", Tuesday, November 5, 1996, printed 11/2/98, 2 pages.
243	EETimes article entitled "Tuner for Toshiba, Toshiba Taps iReady for Internet Tuner", by David Lammers, 2 pages, printed 11/02/98.
244	"Comparison of Novell Netware and TCP/IP Protocol Architectures", by J.S. Carbone, 19 pages, printed 4/10/98.
245	Adaptec article entitled "AEA-7110C-a DuraSAN product", 11 pages, printed 10/1/01.
246	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.
247	iSCSI HBA article entitled "FCE-3210/6410 32 and 64-bit PCI-to-Fibre Channel HBA", 6 pages, printed 10/01/01.
248	ISCSI.com article entitled "iSCSI Storage", 2 pages, printed 10/01/01.
249	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.
250	iReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98, 3 pages.
251	Internet pages of InterProphet entitled "Frequently Asked Questions", by Lynne Jolitz, printed 6/14/99, 4 pages.
252	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
253	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
254	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.

Examiner	Date Considered
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255	Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.
256	CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.
257	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
258	CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.
259	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
260	Internet pages entitled "Technical White Paper-Xpoint's Disk to LAN Acceleration Solution for Windows NT Server, printed 6/5/97, 15 pages.
261	Jato Technologies article entitled "Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98, 13 pages.
262	EETimes article entitled "Enterprise System Uses Flexible Spec, dated August 10, 1998, printed 11/25/98, 3 pages.
263	Internet pages entitled "Smart Ethernet Network Interface Cards", which Berend Ozceri is developing, printed 11/25/98, 2 pages.
264	Internet pages of Xahti corporation entitled "GigaPower Protocol Processor Product Review," printed 11/25/99, 4 pages.
265	U.S. Provisional Patent Application No.: 60/283,896, Titled: CRC Calculations for Out of Order PUDs, Filed April 12, 2003, Inventor: Amit Oren, Assignee: Siliquent Technologies Ltd.
266	Internet pages entitled "DART: Fast Application Level Networking via Data-Copy Avoidance," by Robert J. Walsh, printed 6/3/99, 25 pages.
267	Andrew S. Tanenbaum, Computer Networks, Third Edition, 1996, ISBN 0-13-349945-6.
268	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, 14 pages.
269	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address <a href="http://www.faqs.org/rfcs/rfc2140.html">http://www.faqs.org/rfcs/rfc2140.html</a> , 9 pages, printed 9/20/02.
270	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
271	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
272	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.

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Page 13 of 13

273	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
274	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
275	Chesson et al., IEEE Syposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
276	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
277	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
278	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
279	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
280	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
281	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.
282	Thia et al. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control," Protocols for High Speed Networks, pages 53-68, 1993.
283	U.S. Provisional Patent Application No.: 60/053,240, Titled: TCP/IP Network Accelerator and Method of Use, Filed July 17, 1997, Inventor: William Jolizt et al
284	Thia et al. Publication entitled "A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture," Protocols for High Speed Networks, pages 224-239, 1995.
285	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 pages).
286	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1988 (10 pages).

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